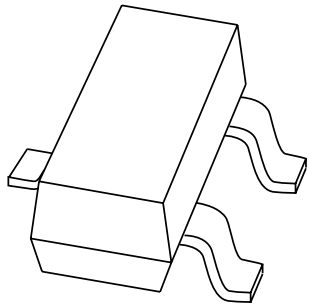


# DATA SHEET



## **BF821; BF823** PNP high-voltage transistors

Product specification  
Supersedes data of September 1994  
File under Discrete Semiconductors, SC04

1997 Apr 23

# PNP high-voltage transistors

# BF821; BF823

### FEATURES

- Low current (max. 50 mA)
- High voltage (max. 300 V).

### APPLICATIONS

- Telephony and professional communication equipment.

### DESCRIPTION

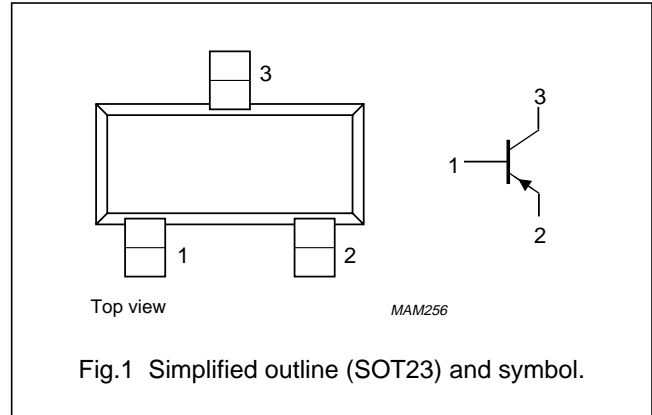
PNP transistor in a SOT23 plastic package.  
NPN complements: BF820, BF822.

### MARKING

TYPE NUMBER	MARKING CODE
BF821	1Wp
BF823	1Yp

### PINNING

PIN	DESCRIPTION
1	base
2	emitter
3	collector



### QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
$V_{CBO}$	collector-base voltage	open emitter			
	BF821		–	–300	V
	BF823		–	–250	V
$V_{CEO}$	collector-emitter voltage	open base			
	BF821		–	–300	V
	BF823		–	–250	V
$I_{CM}$	peak collector current		–	–100	mA
$P_{tot}$	total power dissipation	$T_{amb} \leq 25\text{ }^{\circ}\text{C}$	–	250	mW
$h_{FE}$	DC current gain	$I_C = -25\text{ mA}; V_{CE} = -20\text{ V}$	50	–	
$C_{re}$	feedback capacitance	$I_C = I_c = 0; V_{CB} = -30\text{ V}; f = 1\text{ MHz}$	–	1.6	pF
$f_T$	transition frequency	$I_C = -10\text{ mA}; V_{CE} = -10\text{ V}; f = 100\text{ MHz}$	60	–	MHz

## PNP high-voltage transistors

## BF821; BF823

**LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter			
	BF821		–	–300	V
	BF823		–	–250	V
V <sub>CEO</sub>	collector-emitter voltage	open base			
	BF821		–	–300	V
	BF823		–	–250	V
V <sub>EBO</sub>	emitter-base voltage	open collector	–	–5	V
I <sub>C</sub>	collector current (DC)		–	–50	mA
I <sub>CM</sub>	peak collector current		–	–100	mA
I <sub>BM</sub>	peak base current		–	–50	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C; note 1	–	250	mW
T <sub>stg</sub>	storage temperature		–65	+150	°C
T <sub>j</sub>	junction temperature		–	150	°C
T <sub>amb</sub>	operating ambient temperature		–65	+150	°C

**Note**

1. Transistor mounted on an FR4 printed-circuit board.

**THERMAL CHARACTERISTICS**

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th j-a</sub>	thermal resistance from junction to ambient	note 1	500	K/W

**Note**

1. Transistor mounted on an FR4 printed-circuit board.

**CHARACTERISTICS**

T<sub>j</sub> = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I <sub>CBO</sub>	collector cut-off current	I <sub>E</sub> = 0; V <sub>CB</sub> = –200 V	–	–10	nA
		I <sub>E</sub> = 0; V <sub>CB</sub> = –200 V; T <sub>j</sub> = 150 °C	–	–10	μA
I <sub>EBO</sub>	emitter cut-off current	I <sub>C</sub> = 0; V <sub>EB</sub> = –5 V	–	–50	nA
h <sub>FE</sub>	DC current gain	I <sub>C</sub> = –25 mA; V <sub>CE</sub> = –20 V	50	–	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = –30 mA; I <sub>B</sub> = –5 mA	–	–800	mV
C <sub>re</sub>	feedback capacitance	I <sub>C</sub> = i <sub>c</sub> = 0; V <sub>CB</sub> = –30 V; f = 1 MHz	–	1.6	pF
f <sub>T</sub>	transition frequency	I <sub>C</sub> = –10 mA; V <sub>CE</sub> = –10 V; f = 100 MHz	60	–	MHz

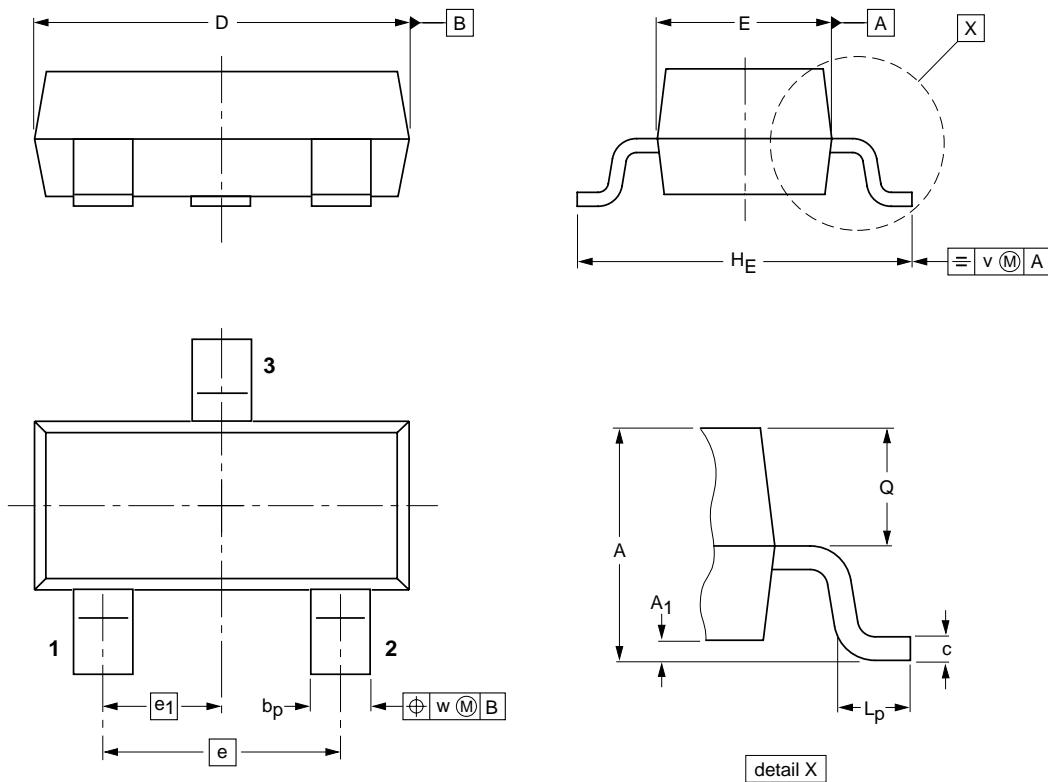
PNP high-voltage transistors

BF821; BF823

PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT23



DIMENSIONS (mm are the original dimensions)

UNIT	A	A <sub>1</sub> max.	b <sub>p</sub>	c	D	E	e	e <sub>1</sub>	H <sub>E</sub>	L <sub>p</sub>	Q	v	w
mm	1.1 0.9	0.1	0.48 0.38	0.15 0.09	3.0 2.8	1.4 1.2	1.9	0.95	2.5 2.1	0.45 0.15	0.55 0.45	0.2	0.1

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT23						97-02-28

## PNP high-voltage transistors

BF821; BF823

**DEFINITIONS**

<b>Data sheet status</b>	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
<b>Limiting values</b>	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
<b>Application information</b>	
Where application information is given, it is advisory and does not form part of the specification.	

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PNP high-voltage transistors

BF821; BF823

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**NOTES**

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PNP high-voltage transistors

BF821; BF823

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**NOTES**

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