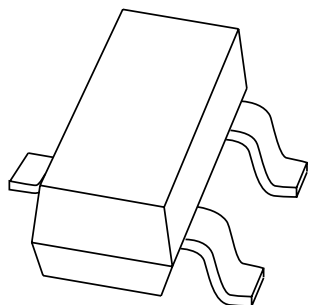


DATA SHEET



PMBTA06 NPN general purpose transistor

Product specification
Supersedes data of September 1994
File under Discrete Semiconductors, SC04

1997 May 28

NPN general purpose transistor

PMBTA06

FEATURES

- High current (max. 500 mA)
- Low voltage (max. 80 V).

APPLICATIONS

- General purpose switching and amplification in e.g. telephony and professional communication equipment.

DESCRIPTION

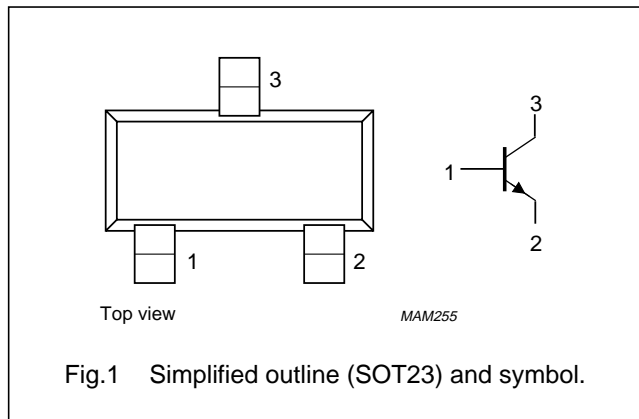
NPN transistor in a SOT23 plastic package.
PNP complement: PMBTA56.

MARKING

TYPE NUMBER	MARKING CODE
PMBTA06	p1G

PINNING

PIN	DESCRIPTION
1	base
2	emitter
3	collector



QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CBO}	collector-base voltage	open emitter	–	80	V
V_{CEO}	collector-emitter voltage	open base	–	80	V
I_{CM}	peak collector current		–	1	A
P_{tot}	total power dissipation	$T_{amb} \leq 25\text{ }^{\circ}\text{C}$	–	250	mW
h_{FE}	DC current gain	$I_C = 100\text{ mA}; V_{CE} = 1\text{ V}$	50	–	
f_T	transition frequency	$I_C = 10\text{ mA}; V_{CE} = 2\text{ V}; f = 100\text{ MHz}$	100	–	MHz

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LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CBO}	collector-base voltage	open emitter	–	80	V
V_{CEO}	collector-emitter voltage	open base	–	80	V
V_{EBO}	emitter-base voltage	open collector	–	4	V
I_C	collector current (DC)		–	500	mA
I_{CM}	peak collector current		–	1	A
I_{BM}	peak base current		–	200	mA
P_{tot}	total power dissipation	$T_{amb} \leq 25\text{ °C}$; note 1	–	250	mW
T_{stg}	storage temperature		–65	+150	°C
T_j	junction temperature		–	150	°C
T_{amb}	operating ambient temperature		–65	+150	°C

Note

1. Transistor mounted on an FR4 printed-circuit board.

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ j-a}$	thermal resistance from junction to ambient	note 1	500	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

CHARACTERISTICS

$T_{amb} = 25\text{ °C}$ unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I_{CBO}	collector cut-off current	$I_E = 0$; $V_{CB} = 60\text{ V}$	–	50	nA
		$I_E = 0$; $V_{CB} = 80\text{ V}$	–	50	nA
I_{EBO}	emitter cut-off current	$I_C = 0$; $V_{EB} = 5\text{ V}$	–	50	nA
h_{FE}	DC current gain	$I_C = 10\text{ mA}$; $V_{CE} = 1\text{ V}$	100	–	
		$I_C = 100\text{ mA}$; $V_{CE} = 1\text{ V}$	100	–	
V_{CEsat}	collector-emitter saturation voltage	$I_C = 100\text{ mA}$; $I_B = 10\text{ mA}$	–	0.25	V
V_{BE}	base-emitter voltage	$I_C = 100\text{ mA}$; $V_{CE} = 1\text{ V}$	–	1.2	V
f_T	transition frequency	$I_C = 10\text{ mA}$; $V_{CE} = 2\text{ V}$; $f = 100\text{ MHz}$	100	–	MHz

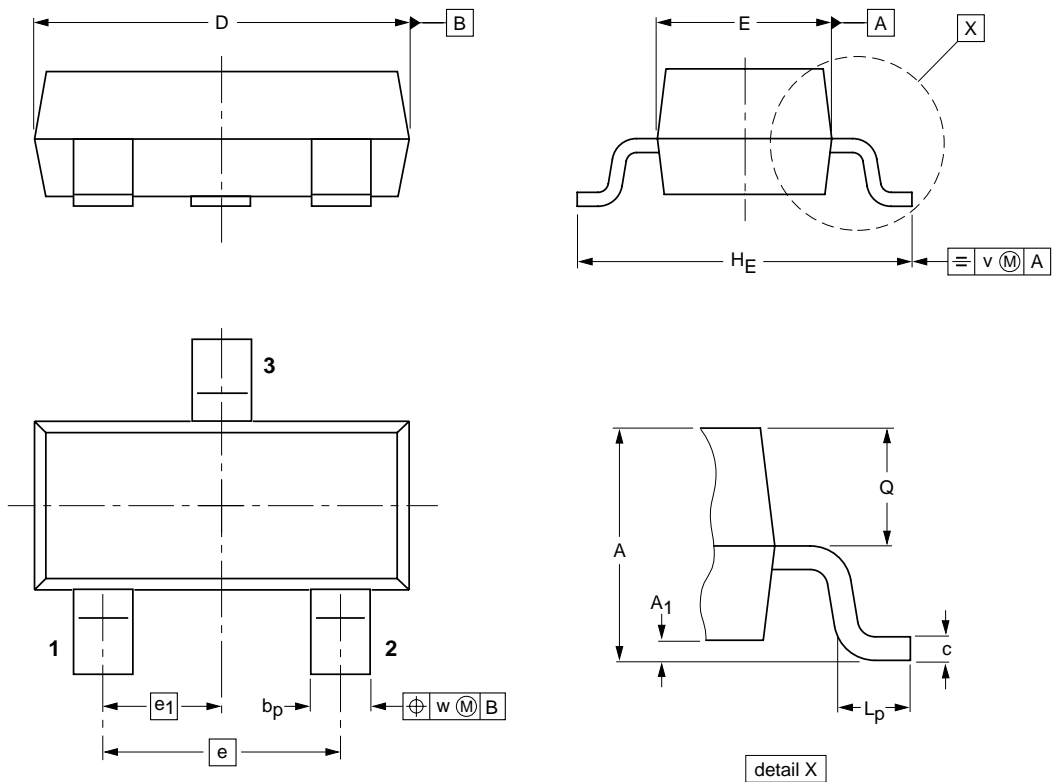
NPN general purpose transistor

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PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT23



DIMENSIONS (mm are the original dimensions)

UNIT	A	A ₁ max.	b _p	c	D	E	e	e ₁	H _E	L _p	Q	v	w
mm	1.1 0.9	0.1	0.48 0.38	0.15 0.09	3.0 2.8	1.4 1.2	1.9	0.95	2.5 2.1	0.45 0.15	0.55 0.45	0.2	0.1

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT23						97-02-28

NPN general purpose transistor

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DEFINITIONS

Data Sheet Status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
Application information	
Where application information is given, it is advisory and does not form part of the specification.	

LIFE SUPPORT APPLICATIONS

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NPN general purpose transistor

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NOTES

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NOTES

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